PRESIDENCY COLLEGE
(AUTONOMOUS)
AFFILIATED TO BENGALURU CITY UNIVERSITY, APPROVED BY AICTE, DELHI & RECOGNISED BY THE GOVT. OF KARNATAKA
RE-ACCREDITED BY NAAC WITH 'A+' GRADE

21C206.2C

REG NO: | | | | | | | | |

END TERM EXAMINATION MAY 2024
BCA – II SEMESTER
GC206.2C: COMPUTER ARCHITECTURE

Duration: 2 Hours **Max Marks: 60**

Instruction: Answers should be written in **English** only.

PART – A

Answer **any EIGHT** questions. **Each** question carries **TWO** marks. **(8 X 2=16)**

1. Convert $101100_{(2)}$ to decimal.
2. Compare combinational and sequential circuits.
3. Define register.
4. Differentiate between encoder and decoder.
5. Define address sequencing.
6. What is control word?
7. List the types of buses.
8. Explicate the term counters.
9. What is paging?
10. Define virtual memory.

PART - B

Answer **any FOUR** questions. **Each** question carries **SIX** marks. **(4 X 6=24)**

1. Explain full adder circuit with a neat diagram.
2. What is JK flip flop? Explain in detail.

3. Elucidate on CISC and RISC.
4. Explain the types of instruction formats.
5. Describe addressing modes with examples.
6. Compare any two types of mapping techniques in cache memory.

PART - C

Answer **any TWO** questions. **Each** question carries **TEN** marks. **(2 X 10=20)**

1. Illustrate 3 X 8 decoder with circuit.
2. Elaborate instruction cycle with flow chart.
3. Describe types of micro operations with example.
4. a) Explain the concept of DMA with suitable diagram. 6M

   b)Discuss memory hierarchy. 4M

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